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Application Number	10806633
Filing Date	2004-03-22
First Named Inventor	Dimpsey et al.
Art Unit	2188
Examiner Name	Portka, Gary J.
Attorney Docket Number	AUS920040062US1

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	3	7181723		2007-02-20	Luk et al.	
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	7	5659679		1997-08-19	Alpert et al.	
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/G.P./	9	6119075		2000-09-12	Dean et al.	
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/G.P./	20	6446029	B1	2002-09-03	Davidson et al.	
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	2	YANG et al., "Improving Performance by Branch Reordering", Proceedings of the ACM SIGPLAN 1998 Conference on Programming Language Design and Implementation, Montreal Canada, 1008, pp. 130-141.	<input type="checkbox"/>
	3	CONTE et al., "Accurate and Practical Profile-Driven Compilation Using the Profile Buffer", Proceedings of the 29th Annual ACM/IEEE International Symposium on Microarchitecture, Paris, France, 1996, pp. 36-45.	<input type="checkbox"/>
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	6	CHANG et al., "Using Profile Information to Assist Classic Code Optimizations", Software Pract. Exper. 21, December 1991, pp. 1301-1321.	<input type="checkbox"/>
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/G.P./	9	INTEL, "Intel IA-64 Architecture Software Developer's Manual", Revision 1.1, Vol. 4, No. 245320.002, July 2001.	<input type="checkbox"/>

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